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James J. Bade (Depositor's name)
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October 6, 2006 (Date)

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/023,123 | 10/30/2001 | Takahiro Fujita | FUJIS 19,099 | 2408 |

TITLE OF INVENTION: PARALLEL SIGNAL AUTOMATIC PHASE ADJUSTING CIRCUIT

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE DUE | PUBLICATION FEE DUE | PREV. PAID ISSUE FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|---------------|---------------------|----------------------|------------------|------------|
| nonprovisional | NO | \$1400 | \$300 | \$0 | \$1700 | 12/07/2006 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|-------------------------|----------|----------------|
| GHEBRETINSAE, TEMESGHEN | 2611 | 375-371000 |

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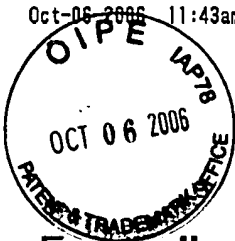
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| October 6, 2006 | 100794-00051 |
| From | Attorney Number |
| James J. Dade | 03290 |
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Comments:

U.S. Patent Application No.: 10/023,123

PARALLEL SIGNAL AUTOMATIC PHASE ADJUSTING CIRCUIT

Our Reference No.: FUJS 19.099 (100794-00051)

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